

STP22NM50 - STP22NM50FP STB22NM50 - STB22NM50-1

N-CHANNEL 500V - 0.16Ω - 20A TO-220/FP/D²PAK/I²PAK MDmesh™Power MOSFET

ADVANCED DATA

TYPE	V _{DSS}	R _{DS(on)}	R _{ds(on)} *Q _g	I _D
STP22NM50	500 V	<0.215Ω	6.4 Ω*nC	20 A
STP22NM50FP	500 V	<0.215Ω	6.4 Ω*nC	20 A
STB22NM50	500 V	<0.215Ω	6.4 Ω*nC	20 A
STB22NM50-1	500 V	<0.215Ω	6.4 Ω*nC	20 A

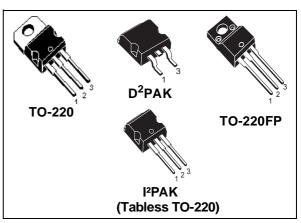
- TYPICAL $R_{DS}(on) = 0.16\Omega$
- HIGH dv/dt AND AVALANCHE CAPABILITIES
- 100% AVALANCHE TESTED
- LOW INPUT CAPACITANCE AND GATE CHARGE
- LOW GATE INPUT RESISTANCE

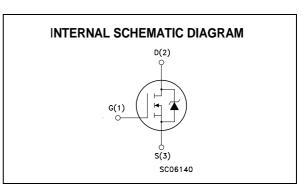
DESCRIPTION

The MDmesh™ is a new revolutionary MOSFET technology that associates the Multiple Drain process with the Company's PowerMESH™ horizontal layout. The resulting product has an outstanding low on-resistance, impressively high dv/dt and excellent avalanche characteristics. The adoption of the Company's proprietary strip technique yields overall dynamic performance that is significantly better than that of similar competition's products.



The MDmesh[™] family is very suitable for increasing power density of high voltage converters allowing system miniaturization and higher efficiencies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Valu	е	Unit
		STP(B)22NM50(-1)	STP22NM50FP	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	500)	V
V_{DGR}	Drain-gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	500)	V
V _{GS}	Gate- source Voltage	±30)	V
I _D	Drain Current (continuous) at T _C = 25°C	20	20(*)	Α
I _D	Drain Current (continuous) at T _C = 100°C	12.6	12.6(*)	Α
I _{DM} (●)	Drain Current (pulsed)	80	80(*)	Α
P _{TOT}	Total Dissipation at T _C = 25°C	192	45	W
	Derating Factor	1.2	0.36	W/°C
dv/dt(1)	Peak Diode Recovery voltage slope	15		V/ns
V _{ISO}	Insulation Winthstand Voltage (DC)		2000	V
T _{stg}	Storage Temperature	-65 to 150		°C
Tj	Max. Operating Junction Temperature	150		°C

 (\bullet) Pulse width limited by safe operating area

(1)I_{SD} \leq 20A, di/dt \leq 400A/µs, V_{DD} \leq V_{(BR)DSS}, T_j \leq T_{JMAX}. (*)Limited only by maximum temperature allowed

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THERMAL DATA

		TO-220/l ² PAK/ D ² PAK	TO-220FP	
Rthj-case	Thermal Resistance Junction-case Max	0.65	2.8	°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max	62	.5	°C/W
T _I	Maximum Lead Temperature For Soldering Purpose	30	00	°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max)	10	А
E _{AS}	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = 5$ A, $V_{DD} = 50$ V)	650	mJ

ELECTRICAL CHARACTERISTICS (T_{CASE} = 25 °C UNLESS OTHERWISE SPECIFIED) OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0$	500			V
I _{DSS}	Zero Gate Voltage	V _{DS} = Max Rating			1	μA
	Drain Current (V _{GS} = 0)	V _{DS} = Max Rating, T _C = 125 °C			10	μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ±30V			±100	nA

ON (1)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	3	4	5	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V, I _D = 10A		0.16	0.215	Ω

DYNAMIC

Symbol	Parameter	Test Conditions Min.		Тур.	Max.	Unit
g _{fs} (1)	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max},$ $I_{D} = 10A$		10		S
C _{iss}	Input Capacitance	$V_{DS} = 25V, f = 1 \text{ MHz}, V_{GS} = 0$		1480		pF
Coss	Output Capacitance			285		pF
C _{rss}	Reverse Transfer Capacitance			34		pF
Coss eq. (2)	Equivalent Output Capacitance	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V$		130		pF
R _g	Gate Input Resistance	f=1 MHz Gate DC Bias=0 Test Signal Level=20mV Open Drain		1.6		Ω

^{1.} Pulsed: Pulse duration = $300 \mu s$, duty cycle 1.5 %.

C_{oss eq.} is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}.

ELECTRICAL CHARACTERISTICS (CONTINUED)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on Delay Time	$V_{DD} = 250 \text{ V}, I_D = 10 \text{ A}$		24		ns
t _r	Rise Time	$R_G = 4.7\Omega V_{GS} = 10 V$ (see test circuit, Figure 3)		16		ns
Qg	Total Gate Charge	$V_{DD} = 400 \text{ V}, I_D = 20 \text{ A},$		40	56	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		13		nC
Q_{gd}	Gate-Drain Charge			19		nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{r(Voff)}	Off-voltage Rise Time	$V_{DD} = 400 \text{ V}, I_D = 20 \text{ A},$		9		ns
t _f	Fall Time	$R_G = 4.7\Omega$, $V_{GS} = 10 \text{ V}$ (see test circuit, Figure 5)		8.5		ns
t _c	Cross-over Time	(coo toot on oun, 1 iguilo o)		23		ns

SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain Current				20	Α
I _{SDM} (2)	Source-drain Current (pulsed)				80	Α
V _{SD} (1)	Forward On Voltage	I _{SD} = 20 A, V _{GS} = 0			1.5	V
t _{rr} Q _{rr} I _{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I_{SD} = 20 A, di/dt = 100A/µs, V_{DD} = 100 V, T_j = 25°C (see test circuit, Figure 5)		350 4.6 26		ns µC A
t _{rr} Q _{rr} I _{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I_{SD} = 20 A, di/dt = 100A/µs, V_{DD} = 100 V, T_j = 150°C (see test circuit, Figure 5)		435 5.9 27		ns µC A

Note: 1. Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %.
2. Pulse width limited by safe operating area.

Fig. 1: Unclamped Inductive Load Test Circuit

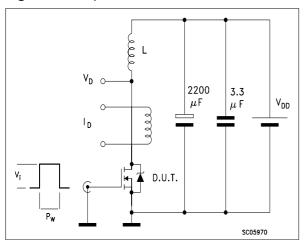


Fig. 3: Switching Times Test Circuits For Resistive Load

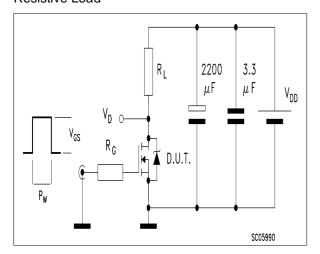


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

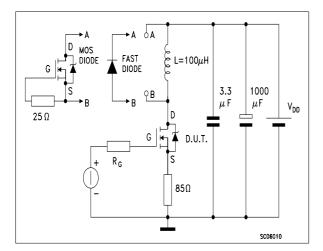


Fig. 2: Unclamped Inductive Waveform

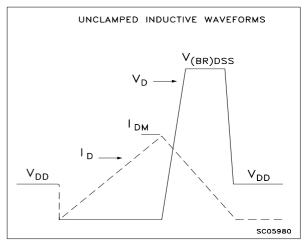
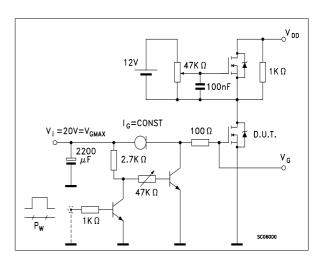
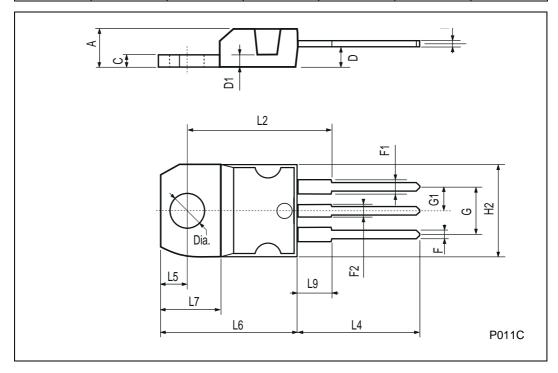


Fig. 4: Gate Charge test Circuit



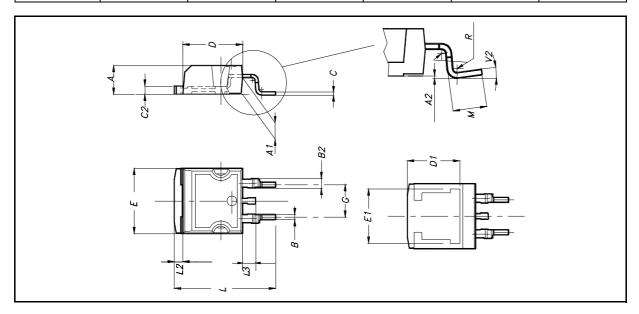
TO-220 MECHANICAL DATA

DIM.		mm			inch	
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
Е	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



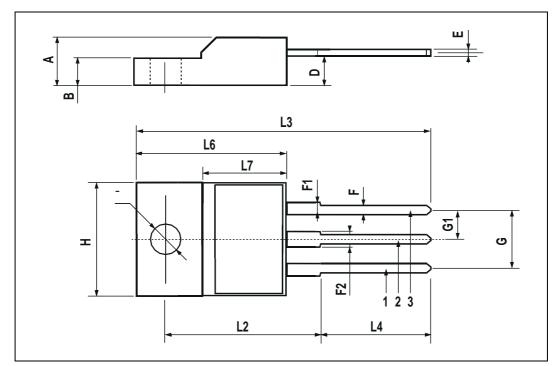
D²PAK MECHANICAL DATA

DIM		mm.			inch	
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
М	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	00		80			



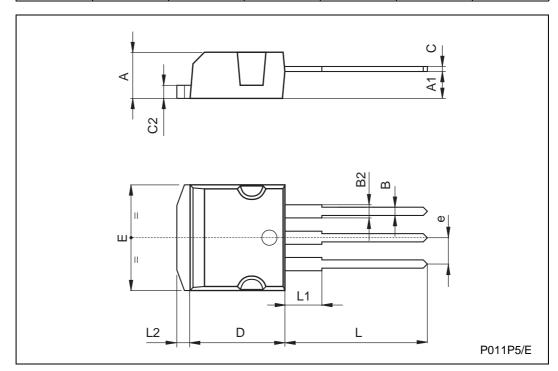
TO-220FP MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
Е	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



TO-262 (I²PAK) MECHANICAL DATA

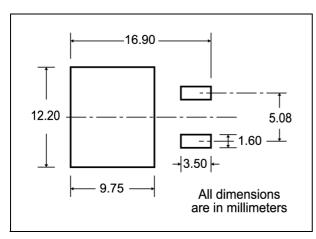
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
е	2.4		2.7	0.094		0.106
E	10		10.4	0.393		0.409
L	13.1		13.6	0.515		0.531
L1	3.48		3.78	0.137		0.149
L2	1.27		1.4	0.050		0.055

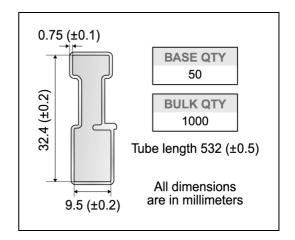


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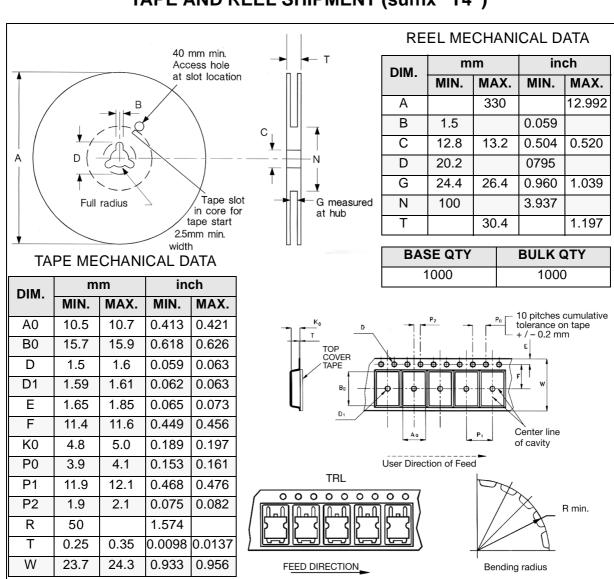
D²PAK FOOTPRINT

TUBE SHIPMENT (no suffix)*





TAPE AND REEL SHIPMENT (suffix "T4")*



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